TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	SYSTEM AND METHOD TO TEST INTEGRATED CIRCUITS ON A WAFER

Application Number :

Date :

First Named Applicant: Lawrence Pileggi

Confirmation Number:

Attorney Docket Number: 361007.000035

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Documents being submitted:	Files
us-request	SRC35-usrequ.xml
	us-request.dtd
	us-request.xsl
us-fee-sheet	SRC35-usfees.xml
	us-fee-sheet.xsl
	us-fee-sheet.dtd
us-declaration	DECPg3.tif
application-body	Document2-trans.xml
	us-application-body.xsl
	application-body.dtd
	wipo.ent
	mathml2.dtd
	mathml2-qname-1.mod
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isogrk3.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
	isotech.ent
	isobox.ent
	isocyr1.ent
	isocyr2.ent
	isodia.ent
	isolat1.ent
	isolat2.ent
	isonum.ent
	isopub.ent
	mmlextra.ent
	mmlalias.ent
	soextblx.dtd
	DrwgPg1.tif
	DrwgPg2.tif
	DrwgPg3.tif
	DrwgPg4.tif
	DrwgPg5.tif
	DrwgPg6.tif
us-declaration	DECPg1.tif
us-declaration	DECPg2.tif

Comments

Included with this submission are the following: 33 paragraphs of specs., 70 claims, Abstract, 6 sheets of drawing, Figs. 1-6; 3 page Declaration, Application Data Sheet, Fee Transmittal and Transmittal documents.